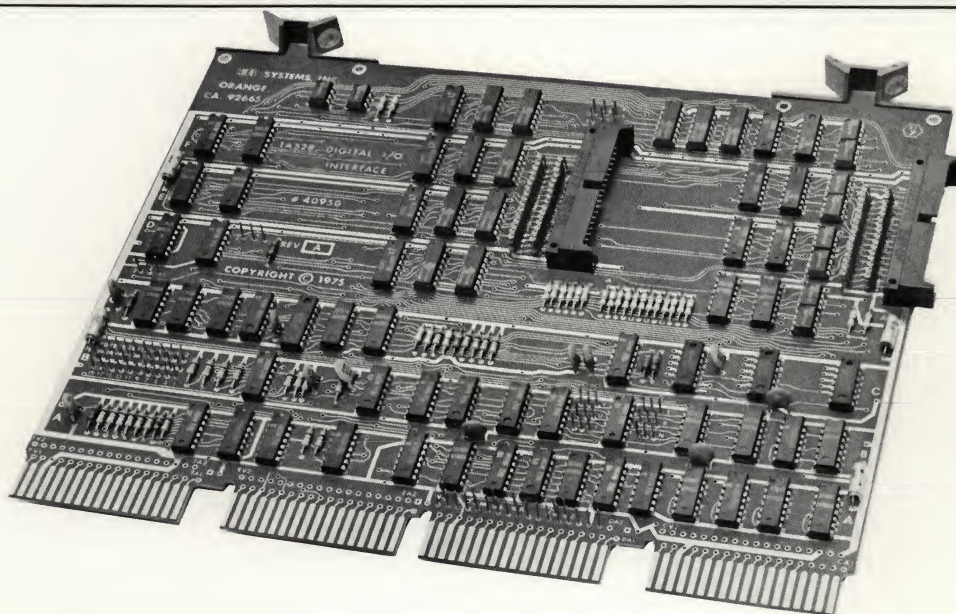# MDB

TA-528 GENERAL PURPOSE INTERFACE for use with PDP*-11 Computers

MDB General Purpose Interface for PDP*-11 Computers

- PDP-11 general purpose bus-to-bus interface, up to 100 feet
- 16-bit data transfer to and from an external peripheral device, up to 100 feet
- Interrupt request and control
- Control and status register
- Address selection
- Output buffer register and input buffer gates
- Line drivers and receivers

The MDB Systems TA-528 general purpose interface module controls data transfer between the DEC PDP-11 Unibus* and user peripheral devices.

The MDB TA-528 module contains all the necessary hardware for control and status register assignments. The TA-528 module is connected via two convenient 40 pin Berg connectors to a peripheral unit. The TA-528 module with appropriate software will control the 16-bit data exchange between the Unibus and various peripheral equipment.

Interrupt request and control logic, address selection, and device interface logic comprise the main functional sections of the MDB TA-528.

Three registers consisting of an input buffer (16-bit read-only), an output buffer (16-bit read/write) and control/status (16-bit read/write), form the interface logic circuits of the TA528 module.

Convenient data exchange between two PDP-11 computers can be achieved by cabling two TA-528 modules together. Using this method a continuous and dependable exchange of data is accomplished and each module maintains transparency to the host computer.

ELECTRICAL REQUIREMENTS:
- +5V, at 1.5A

ACCESSORIES:
- Optional general purpose cables available

PHYSICAL:
- Occupies 1 quad slot of standard system unit

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